

SLB120N06T

60V N-Channel MOSFET

General Description

This Power MOSFET is produced using Msemitek's advanced TRENCH technology.

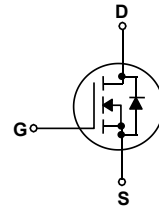
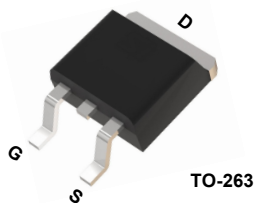
This advanced technology has been especially tailored to minimize conduction loss, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

Application

- PWM Application
- Load Switch
- Power Management

Features

- 120A, 60V, $R_{DS(on)Typ} = 4.4m\Omega @ V_{GS} = 10V$
- Very Low On-resistance RDS(ON)
- High ruggedness
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



Absolute Maximum Ratings

$T_C = 25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | SLB120N06T | Units |
|-----------------|---|-------------|---------------------------|
| V_{DSS} | Drain-Source Voltage | 60 | V |
| I_D | Drain Current - Continuous ($T_C = 25^\circ\text{C}$) | 120 | A |
| | | 78 | A |
| I_{DM} | Drain Current - Pulsed (Note 1) | 360 | A |
| V_{GSS} | Gate-Source Voltage | ± 20 | V |
| EAS | Single Pulsed Avalanche Energy (Note 2) | 500 | mJ |
| P_D | Power Dissipation ($T_C = 25^\circ\text{C}$) | 230 | W |
| $R_{\theta JC}$ | Thermal Resistance, Junction-to-Case | 0.55 | $^\circ\text{C}/\text{W}$ |
| T_J, T_{STG} | Operating and Storage Temperature Range | -55 to +150 | $^\circ\text{C}$ |
| T_L | Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds | 300 | $^\circ\text{C}$ |

* Drain current limited by maximum junction temperature.

Package Marking

| Part Number | Top Marking | Package | Packing Method | MOQ | QTY |
|-------------|-------------|---------|----------------|-----|------|
| SLB120N06T | SLB120N06T | TO-220C | Tape & Reel | 800 | 4000 |

Electrical Characteristics

$T_C = 25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Test Conditions | Min | Typ | Max | Units |
|--------|-----------|-----------------|-----|-----|-----|-------|
|--------|-----------|-----------------|-----|-----|-----|-------|

Off Characteristics

| | | | | | | |
|------------|------------------------------------|---|----|----|------|---------------|
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$ | 60 | -- | -- | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{DS} = 60\text{ V}, V_{GS} = 0\text{ V}$ | -- | -- | 1 | μA |
| | | $V_{DS} = 48\text{ V}, T_C = 150^\circ\text{C}$ | -- | -- | 10 | μA |
| I_{GSSF} | Gate-Body Leakage Current, Forward | $V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$ | -- | -- | 100 | nA |
| I_{GSSR} | Gate-Body Leakage Current, Reverse | $V_{GS} = -20\text{ V}, V_{DS} = 0\text{ V}$ | -- | -- | -100 | nA |

On Characteristics

| | | | | | | |
|--------------|-----------------------------------|---|----|-----|-----|------------|
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$ | 2 | -- | 4 | V |
| $R_{DS(on)}$ | Static Drain-Source On-Resistance | $V_{GS} = 10\text{ V}, I_D = 30\text{ A}$ | -- | 4.4 | 5.4 | m Ω |

Dynamic Characteristics

| | | | | | | |
|------------|------------------------------|--|----|------|----|----|
| C_{iss} | Input Capacitance | $V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$ | -- | 5540 | -- | pF |
| C_{oss} | Output Capacitance | | -- | 420 | -- | pF |
| C_{riss} | Reverse Transfer Capacitance | | -- | 390 | -- | pF |

Switching Characteristics

| | | | | | | |
|--------------|---------------------|--|----|-----|----|----------|
| $t_{d(on)}$ | Turn-On Delay Time | $V_{DD} = 30\text{ V}, I_D = 60\text{ A},$ $R_G = 1\text{ }\Omega, R_L = 0.4\text{ }\Omega$ (Note 3) | -- | 17 | -- | ns |
| t_r | Turn-On Rise Time | | -- | 16 | -- | ns |
| $t_{d(off)}$ | Turn-Off Delay Time | | -- | 35 | -- | ns |
| t_f | Turn-Off Fall Time | | -- | 13 | -- | ns |
| Q_g | Total Gate Charge | $V_{DS} = 30\text{ V}, I_D = 60\text{ A},$ $V_{GS} = 10\text{ V}$ (Note 3) | -- | 141 | -- | nC |
| Q_{gs} | Gate-Source Charge | | -- | 37 | -- | nC |
| Q_{gd} | Gate-Drain Charge | | -- | 47 | -- | nC |
| R_G | Gate Resistance | $f = 1\text{ MHz}$ | -- | 1.7 | -- | Ω |

Drain-Source Diode Characteristics and Maximum Ratings

| | | | | | | |
|----------|---|--|----|-----|-----|---|
| I_S | Maximum Continuous Drain-Source Diode Forward Current | -- | -- | 120 | A | |
| I_{SM} | Maximum Pulsed Drain-Source Diode Forward Current | -- | -- | 360 | A | |
| V_{SD} | Drain-Source Diode Forward Voltage | $V_{GS} = 0\text{ V}, I_S = 60\text{ A}$ | -- | -- | 1.2 | V |

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition: $T_J = 25^\circ\text{C}, V_{DD} = 20\text{ V}, V_G = 10\text{ V}, L = 0.5\text{ mH}$.
3. Pulse Test: Pulse Widths $\leq 300\text{ }\mu\text{s}$, Duty Cycle $\leq 0.5\%$

N- Channel Typical Characteristics

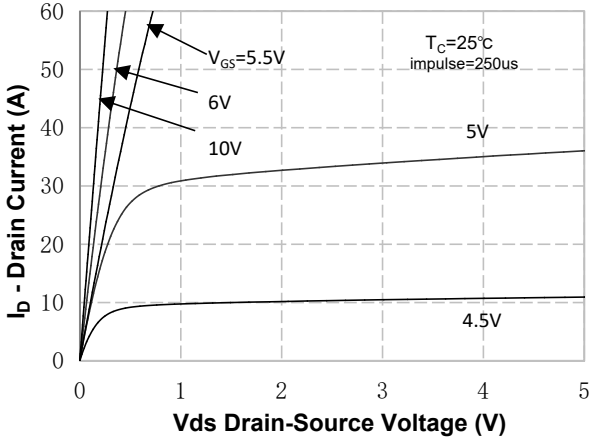


Figure 1. On-Region Characteristics

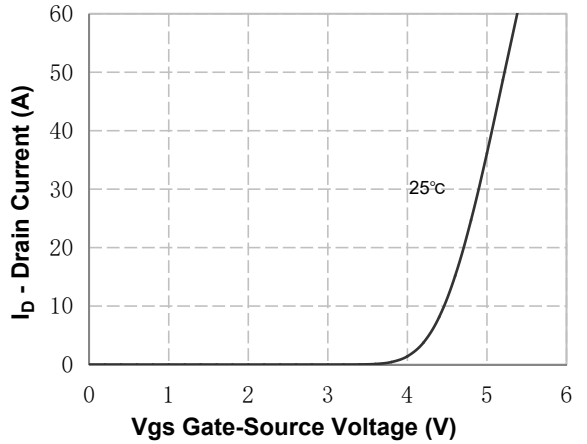


Figure 2. Transfer Characteristics

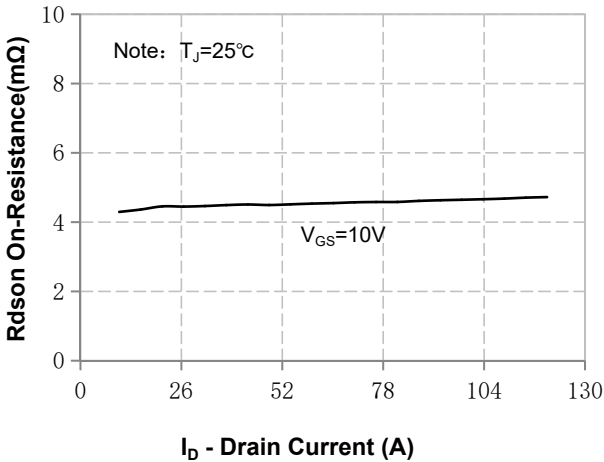


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

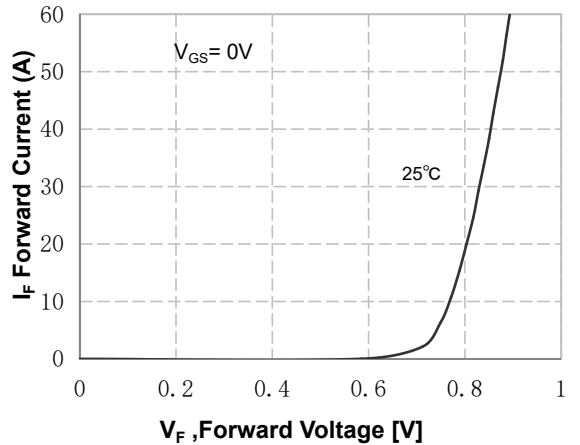


Figure 4. Body Diode Forward Voltage Variation with Source Current

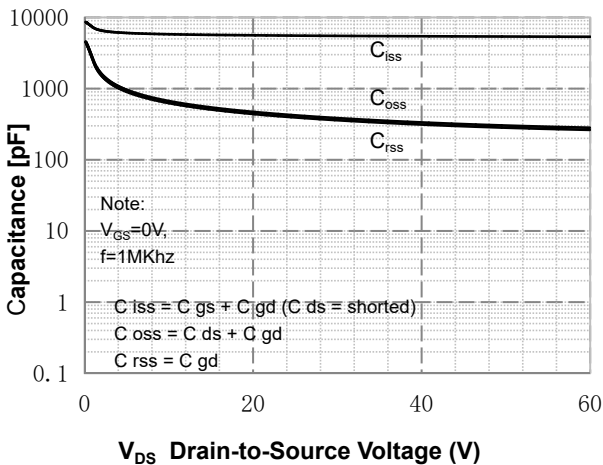


Figure 5. Capacitance Characteristics

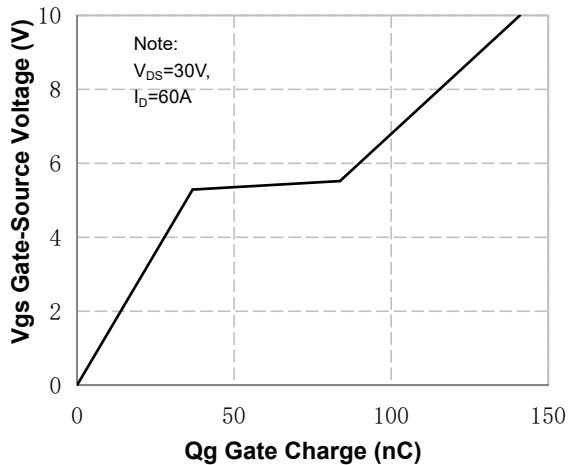


Figure 6. Gate Charge Characteristics

N- Channel Typical Characteristics (Continued)

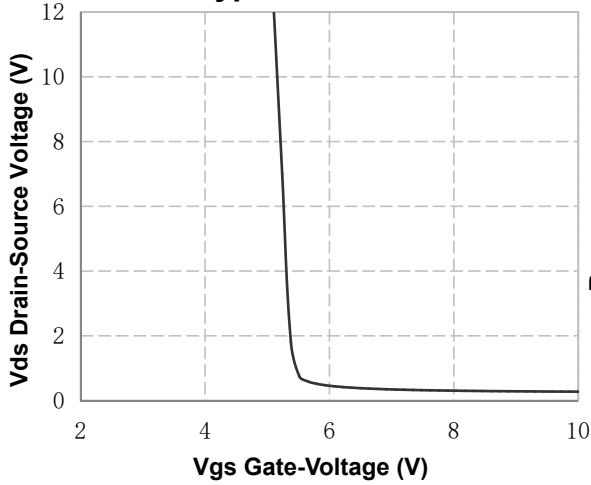


Figure 7. Vds Drain-Source Voltage vs Gate Voltage

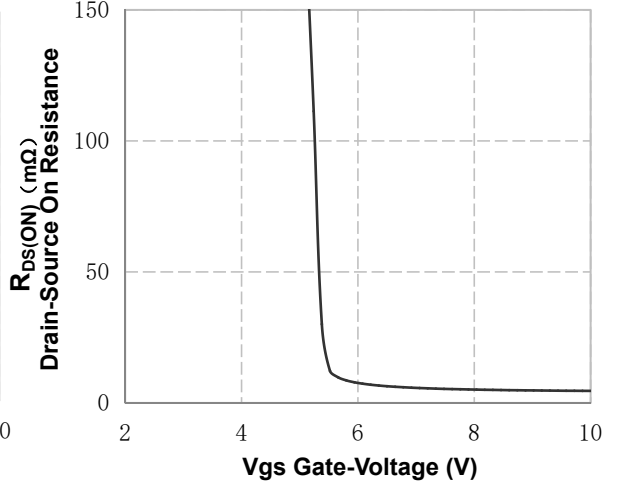


Figure 8. On-Resistance vs Gate Voltage

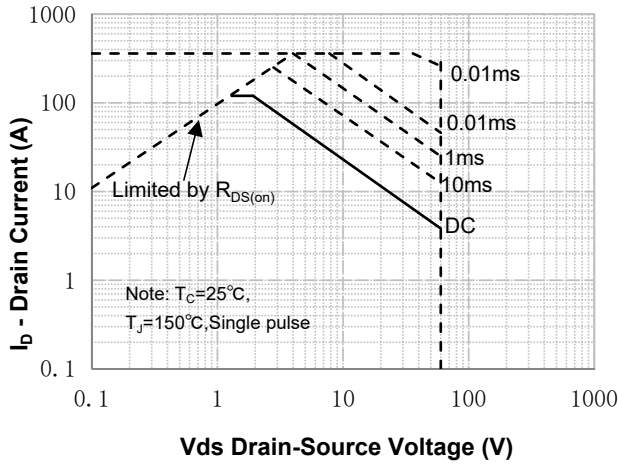


Figure 9. Maximum Safe Operating Area

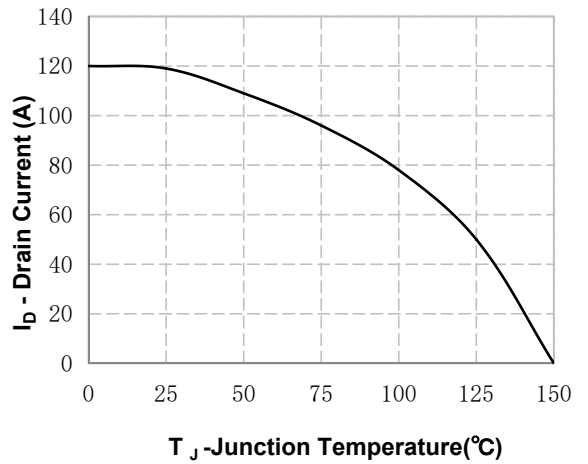


Figure 10. Maximum Continuous Drain Current vs Temperature

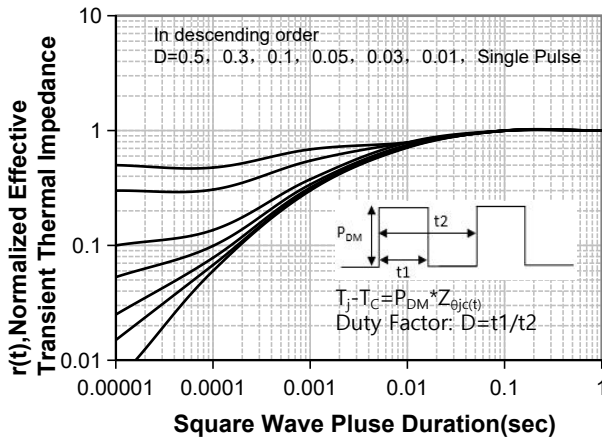
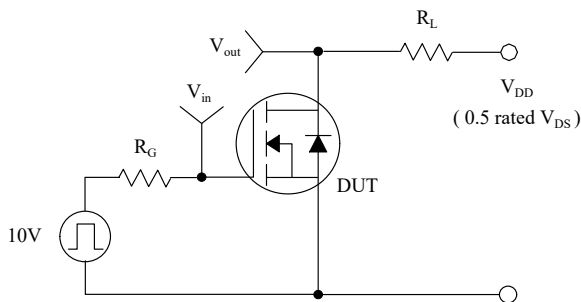


Figure 11. Transient Thermal Response Curve

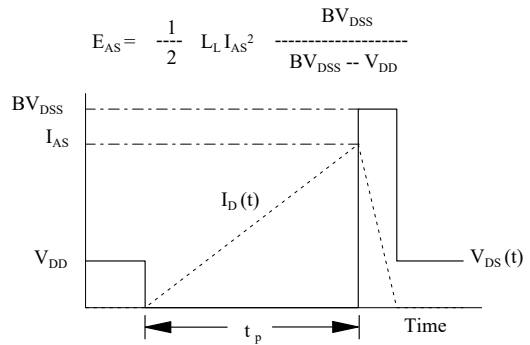
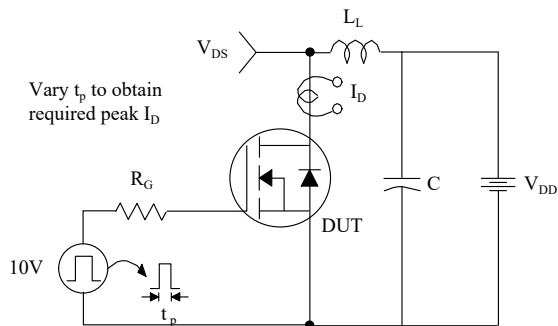
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



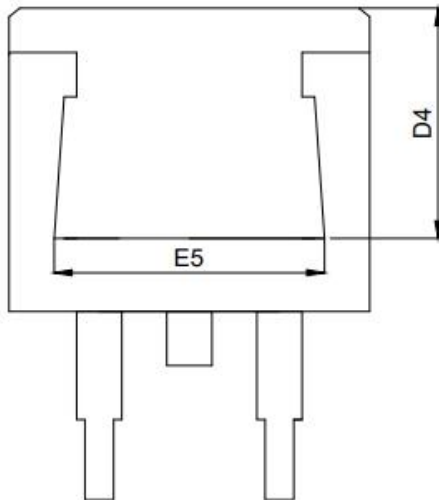
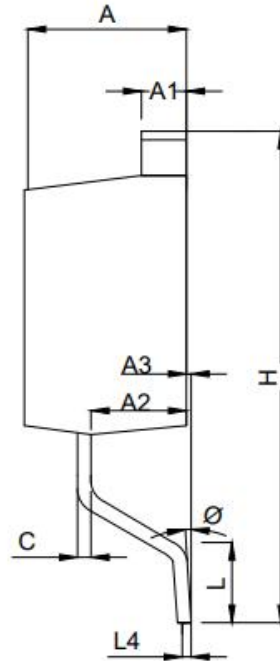
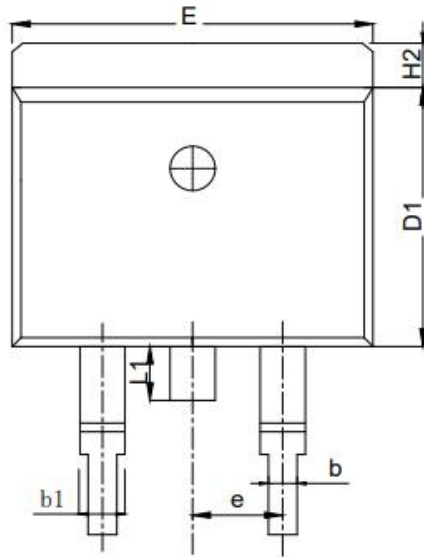
Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Test Circuit & Waveforms



TO-263 OUTLINE



| Symbol | DIMENSIONS (unit:mm) | | |
|--------|----------------------|-------|-------|
| | Min | Typ | Max |
| A | 4.37 | 4.57 | 4.77 |
| A1 | 1.22 | 1.27 | 1.42 |
| A2 | 2.49 | 2.69 | 2.89 |
| A3 | 0 | 0.13 | 0.25 |
| b | 0.7 | 0.81 | 0.96 |
| b1 | 1.17 | 1.27 | 1.47 |
| c | 0.3 | 0.38 | 0.53 |
| D1 | 8.5 | 8.7 | 8.9 |
| D4 | 6.6 | - | - |
| E | 9.86 | 10.16 | 10.36 |
| E5 | 7.06 | - | - |
| e | 2.54 BSC | | |
| H | 14.7 | 15.1 | 15.5 |
| H2 | 1.07 | 1.27 | 1.47 |
| L | 2 | 2.3 | 2.6 |
| L1 | 1.4 | 1.55 | 1.7 |
| L4 | 0.25 BSC | | |
| ? | 0° | 5° | 9° |

NOTE:

- 1.The plastic package is not marked as smooth surfaceRa=0.1;Subglossy surfaceRa=0.8
- 2.Undeclared tolerance ±0.25,Unmarked filletRmax=0.25

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